ABSTRACT OF THE DISCLOSURE

While image data is written into either one of first, second, third frame memories 1, 2 and 3, image data are repetitively read two times from the remaining two memories in one vertical synchronization interval and transferred to an arithmetic unit 4, and this operation is executed with the frame memories changed sequentially. An arithmetic unit 4 refers to a look-up table on the basis of two inputted data values and, when the data value of the current image signal is greater than the data value of the previous image signal, the unit 4 transfers image data of a value greater than the data value of the current image signal to a liquid crystal display device 5. Thus, the step response characteristic is improved for the improvement of the dynamic image display quality.